Training Agenda – Day 1

Description	Exercise
Classes	1
Queries with Design Rules	2
Clearance Checking in 3D	3
BREAK (15 min)	
Differential Pair Routing	4
Re-routing	5
LUNCH	
Re-routing	6
ActiveRoute	7
High Speed Design with xSignals	8
BREAK (15 min)	
Back Drilling	9
Via Stitching/Shielding	10

Altium

Training Agenda – Day 2

Description	Exercise
Teardrops	11
PCB Layer Stack Management	12
Rigid-flex Design	13
BREAK (15 min)	
Snippets	14
Multi-channel Design	15 & 16
LUNCH	
Board Level Annotation	17
Variants	18
Pin/Part Swapping	19
BREAK (15 min)	
Schematic Templates	Additional
PCB and PCB Library Templates	Additional

Training Agenda – Day 3

Description	Exercise
Bill of Material Templates	Additional
Draftsman Templates	20
PCB Project Template	21
BREAK (15 min)	
Defining Board Shape from 3D	22
Testpoints	23
LUNCH	
Embedded Board Arrays	24
Design Views	25
Additional Topics	
Version Control	-
Device Sheets	-